

One Week
Faculty Development Program (FDP)
on
“Advanced VLSI System Design
& Challenges”

22nd - 26th August, 2022.

REGISTRATION FORM

Name: _____

Designation: _____

Mobile: _____

College Name: _____

E-Mail: _____

Experience: _____

Registration Category:

Academician Research scholar

Industry delegate PG Student

Is Accommodation required?

Yes No

Signature of the Applicant

DECLARATION

Dr./Mr./Ms: _____

is a bonafide Faculty/Employee/Research scholar/PG Student of our College/Institute/ University. He/She can attend the FDP for the entire duration.

Place:

Date:

Signature of the
Head of the Institution (with Seal)

Important Dates:

Last Date for Registration: **19th August, 2022**

Date of Confirmation: **20th August, 2022**

Who Shall Participate?

The FDP is aimed at the multifaceted audience from industry, full time research scholars, PG students and academicians from various Engineering colleges of ECE, EEE, CSE and IT departments.

Registration fee:

| | |
|--------------------------------------------|------------|
| Faculty, Research Scholars and PG Students | Rs. 300/- |
| Industry participants | Rs. 1000/- |

Registration fee may be remitted Online through NEFT to the Bank account given below.

| | |
|------------------|-----------------------------------|
| Account Name | Narasaraopeta Engineering College |
| Name of the Bank | Axis Bank |
| Account No | 476010100026877 |
| IFSC | UTIB0000476 |

Accommodation:

Limited accommodation will be provided for outstation participants on prior request with nominal fee.

Note: No TA will be paid to outstation participants.

Scan for Registration (Online):



<https://tinyurl.com/NECECEVLSIFDP>

Contact Details:

Dr K. Raju, Professor
Mobile No: 9493451237

One Week
Faculty Development Program (FDP)
on
“Advanced VLSI System Design
& Challenges”

22nd - 26th August, 2022

In association with



Organized by

**Department of
Electronics & Communication
Engineering**



**NARASARAOPETA ENGINEERING COLLEGE
(AUTONOMOUS)**

Kotappakonda Road, Yellamanda (P.O)
Narasaraopet – 522601, Palnadu District, A.P.
Approved by AICTE, Permanently Affiliated to JNTUK, Kakinada,
Accredited ‘A+’ Grade by NAAC, Accredited by NBA.

Phone : **08647-239914**

FAX : **08647-239902**

E-Mail : **ecehod@nrtec.in**

ABOUT THE COLLEGE

The Narasaraopeta Engineering College was established in the year 1998 by Gayatri Educational Development Society, Narasaraopet, under the leadership of Sri M.V.Koteswara Rao, who has a vision of developing this college as an autonomous institution of higher learning & knowledge in the heart of Palnadu region. The college is approved by AICTE, permanently affiliated to JNTUK Kakinada, Accredited by NAAC with 'A+' Grade and ECE & CSE courses are Accredited four times by NBA. NEC is announced as 'AUTONOMOUS' in the year 2016. The college got NIRF ranking in the band of 251-300 by MHRD. The college has launched Incubation Centre where the students can implement their ideas. The college has AP CM's Skill Excellence Centre, AI&ML centre and MSME centre. The college offers 10 courses at B.Tech level in the branches **Civil, EEE, ME, ECE, CSE, CSE(AI), CSE(AI&ML), CSE(DS), CSE(Cyber Security)** and **IT**. Also, the college offers PG programmes with 7 specializations of M.Tech., MCA and MBA. The college has well equipped laboratories including 1000 computers with a bandwidth of 100 MBPS internet. Microsoft Innovation Centre was established in the college.

In order to build a vibrant entrepreneurship ecosystem Venture Development Centre (VDC) is established in association with i2E North Eastern University, USA and APSSDC. Also, the college has Indo-European skilling Centre for Mechatronics and Industrial Robotics in collaboration with APSSDC and ARC (Applied Robot Control), Germany. AP CM's Skill Excellence Center is also established to provide students access to the most advanced courses by industry giants. The college has MOU with New Mexico State University, USA, which enables student and faculty exchange programs.

Narasaraopet is known as the gateway of Palnadu. Narasaraopet is the district head quarter of Palnadu district and it is on the railway line of Guntur to Guntakal. Narasaraopet is 50 Km from Guntur and 25 Km from Chilakaluripet (NH5) by road. Narasaraopet is 120 Km away from Vijayawada Airport. Kotappakonda is the site of major religious pilgrim place of Lord Siva which is located 5 Km from NEC.

ABOUT THE DEPARTMENT

The **Department of Electronics and Communication Engineering** was started in the year 1998 with an intake of 40 students in **B.Tech.** programme. The intake was enhanced subsequently and the present intake is 240. The department also offers two PG programmes, **M.Tech** (Digital Electronics and Communication Systems) which was started in the year 2004 with an intake of 18 and **M.Tech** (Digital Systems and Computer Electronics) which was started in the year 2006 with an intake of 18. The department has a team of highly qualified and experienced teaching faculty.

The department has good infrastructural facilities and has full-fledged laboratories equipped with adequate state-of-the-art equipment and latest software. The UG programme of the department is accredited Four times by AICTE-NBA. The department is recognized as Research Center by JNTUK, Kakinada. The department has Embedded Systems and Robotics Lab which was established under IIT Bombay E-Yantra Project. The department has vibrant atmosphere for the students to emphasize on fundamentals and concepts focusing on analysis and design of various applications to fulfill the needs of industry. The department encourages faculty members and research scholars to carry out their research.

ABOUT THE FDP

Advances in VLSI technology and its challenges make the academicians and industrialists to work in the field of VLSI design for high performance VLSI systems. This FDP will give the participants a valuable resource for enhancing their knowledge about the current issues in the VLSI design for industry requirement in various applications. The aim of FDP is to provide a platform, where researchers and academicians can get hands-on training in Cadence tool and share their knowledge with each other and enhance their academic and research skills in the field of VLSI design.

TOPICS TO BE COVERED

- VLSI Signal processing
- Design Challenges in hardware Implementation
- On-Chip ML based accelerators
- Semi & Full Custom IC Design Flow
- Layout Design
- Physical Implementation
- AMS Design Flow
- Fin FET Technology

RESOURCE PERSONS

Dr. Ch. Srinivasa Rao, Professor & Principal

JNTUK UCEN, Narasaraopet, AP.

Dr. Ramesh Vaddi, Assoc. Prof. & Head,

Dept, of ECE, SRM University AP.

Mr. M. Nagaraju, AE-Manager

Entuple Technologies, Bengaluru.

Mr. B.K. Shiva Prasad, Sr. FAE

Entuple Technologies, Bengaluru.

Mr. V. Navaneet Krishan, Sr. FAE

Entuple Technologies, Bengaluru.

Mr. K. Sudhir Kumar, FAE

Entuple Technologies, New Delhi.

Dr. K. Raju, Professor, Dept. of ECE

Narasaraopeta Engineering College, Narasaraopet.

Chief Patron

Sri M.S. Chakravarthi

B.E., M.S(USA), Exec-MBA(ISB)

Vice - Chairman, NEC Group.

Patrons

Sri M.V. Koteswara Rao B.Sc.

Chairman, NEC Group.

Sri M. Ramesh Babu

Secretary, NEC Group.

Co-Patrons

Dr. M. Sreenivasa Kumar

M.Tech., Ph.D. (UK), MISTE., FIE(I)

Principal.

Dr. D. Suneel

M.Tech., Ph.D

Vice-Principal.

Convener

Dr. V. Venkata Rao

Professor & Head, Dept. of ECE.

Coordinator

Dr. K. Raju

Professor, Dept. of ECE.

Organizing Committee

Dr. J. V. K Ratnam

Dr. Amit Gupta

Dr. A.V. Nageswara Rao

Dr. R. Samba Siva Nayak

Dr. Sk. Bajid Vali

Dr. Y. Jaipal Reddy

Mr. P. S.S.Chakravarthi

Mr. J. Narasimha Rao

Mr. V. Naveen Raja

Mr. P. Bhagya Raju

Mr. N. Srinivasa Rao

Mr. Sk. Zuber Basha

Mrs. T. Indira

Mr. A. Raveendra Babu

Mrs. B. Suneetha

Mr. M.H.H. Sastry

Mr. B. Siva Nageswara Rao

Mr. B. Srinivasa Rao

Mr. Ch. Adibabu

Mrs. Sk. Areefa Begam

Mr. M. Suneel Raja

Mr. Sk. Md. Gouse

Mr. Sk. Mastan Shareef

Mrs. G. Rajavali

Mr. G.S.J. Victor

Mr. Sk. Md. Shareef

Mrs. P. Vinuthna

Mr. P. Jaya Babu

